The FMCAD 2016 Graduate Student Forum

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Computer Science Department
Rochester Institute of Technology

Abstract—The FMCAD Student Forum provides a platform for graduate students at any career stage to introduce their research to the wider Formal Methods community, and solicit feedback. The 2016 student forum is the second in the series to feature a Best Contribution Award (based on the quality of the submission, the poster, and the presentation), announced during the conference and publicized on the FMCAD website. Since 2013, the FMCAD conference features a Student Forum, providing a platform for graduate students at any career stage to introduce their research to the wider Formal Methods community. The FMCAD 2016 Graduate Student Forum follows the tradition of its predecessors, which took place in Austin, Texas, USA in 2015 [1], in Lausanne, Switzerland in 2014 [2] and in Portland, Oregon, USA in 2013 [3].

Graduate students were invited to submit short reports describing their ongoing research in the scope of the FMCAD conference. The submissions to the forum presented novel technical contributions and outlining future research planned by the authors. The presentations covered a broad range of topics in the field of verification and synthesis, including automated reasoning, model checking of hardware, software, as well as hybrid systems, verification and synthesis of networks, and application of artificial intelligence techniques to circuit design. Based on the reviews provided by members of the organizing committee as well as a number of external reviewers, 10 submissions were accepted. The reviews focused on the novelty of the work, the technical maturity of the presentation, and the quality and soundness of the presentation. The following contributions have been accepted:

- Elaheh Ghassabani, Michael W. Whalen, Andrew Gacek, Rockwell Collins: “Inductive Validity Cores for Formal Verification”
- Yu-Yun Dai, Robert Brayton: “Circuit Recognition with Convolutional Neural Networks”
- Bo-Yuan Hunag, Pramod Subramanyan, Sharad Malik, Sayak Ray, Hareesh Khattri, Jason Fung, Abhranil Maiti: “Instruction-Level Abstraction Based SoC Firmware Verification”
- William Hallahan, Ruzica Piskac, Ennan Zhai, Avi Silberschatz: “Automated Firewall Repair with Example Synthesis”
- Hongce Zhang, Sharad Malik: “Equivalence Checking Using the Intermediate Instruction-Level Abstraction”
- Baoluo Meng: “Solving Relational Constraints with Extensions to a Theory of Finite Set in SMT”
- Mark Santolucito, Ruzica Piskac: “Version Space Learning for Verification on Temporal Differentials”
- Jaideep Ramachandran: “Precise Arithmetic Reasoning using Approximate Solvers”
- Rohit Dureja, Kristin Rozier: “Comparative Safety Analysis of Wireless Communication Networks in Avionics”
- Andres Noetzli: “Proofs for Preprocessing in SMT Solvers”

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REFERENCES


1http://www.cs.utexas.edu/users/hunt/FMCAD/FMCAD16/